

## **IEEE Section(s) monitoring the plaque(s):** (2019-05)

### **The facts are as follows:**

The IEEE Kharagpur (IIT) Section in India is a '*sovereign*' small IEEE Section (unique to entire IEEE) in the eastern part of India confined to Indian Institute of Technology at Kharagpur, a single premier technological institution, which is seventy two miles (72) south-west of the city of Calcutta (Kolkata).

**It is like the sovereign 'Vatican City' in 'Rome' Italy.**

A part of the *escrow account* fund of the IEEE Kharagpur (IIT) Section currently being held in the U.S.A. (as Sir J. C. Bose Memorial Fund) is establishing a Sir J. C. Bose Memorial Lecture to be jointly managed by the IEEE Kharagpur Section and the Sir J. C. Bose Trust at the Sir J. C. Bose's home, at least once a year. Through this continuous relationship, the IEEE Kharagpur (IIT) Section will also be monitoring the IEEE Milestone Plaque.

*Every IEEE Milestone case is unique in its details.* The IEEE Milestone Proposal 2019-05 is about invention of a semiconductor

device. It is **not** about a stationary structure confined to a specific place, existing or extinct. The resulting IEEE Plaque will be inside a secure place. It will not be in an unattended open place that could be subject to vandalism. Therefore, the monitoring requirement is more of a token administrative requirement, which will be fully met by the IEEE Kharagpur (IIT) Section described below:

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The IEEE Kharagpur Section is a section in India Council under Region 10 of the IEEE organization. **It is a unique section in the whole IEEE as the section is rooted at the Indian Institute of Technology Kharagpur, unlike other sections where activities usually spread over a number of associations.** The section office is located on the campus of the Indian Institute of Technology Kharagpur.

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[This reply is prepared and sent by Dr. Probir K. Bondyopadhyay (2019-05) proposer]